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APPLICATION NO.	FILING	DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/051,495	01/18/2002		David Carroll Challener	, RPS920010160US1	1606	
25299	7590	06/30/2005		EXAM	IINER	
IBM CORPO	RATION			WILLIAMS,	JEFFERY L	•
PO BOX 1219	5				<u> </u>	_
DEPT YXSA,	<b>BLDG 002</b>			ART UNIT	PAPER NUMBER	
		DADY NO 277	20	2122		

DATE MAILED: 06/30/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
Office Action Summany	10/051,495	CHALLENER ET AL.			
Office Action Summary	Examiner	Art Unit			
	Jeffery Williams	2137			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet wit	th the correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a re y within the statutory minimum of thirty vill apply and will expire SIX (6) MON , cause the application to become AB	eply be timely filed  (30) days will be considered timely.  THS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).			
Status		•			
Responsive to communication(s) filed on 18 Ja     This action is FINAL 2b) ☐ This     Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final.	• • •			
Disposition of Claims					
4) ⊠ Claim(s) 1-15 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-15 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or	wn from consideration.				
Application Papers					
9) ☐ The specification is objected to by the Examiner.  10) ☐ The drawing(s) filed on 4/5/02 is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.					
Priority under 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents</li> <li>2. Certified copies of the priority documents</li> <li>3. Copies of the certified copies of the priority application from the International Bureau</li> <li>* See the attached detailed Office action for a list of the certified copies of the attached detailed Office action for a list of the certified copies</li> </ul>	s have been received. s have been received in Aprity documents have been u (PCT Rule 17.2(a)).	oplication No received in this National Stage			
·					
Attachment(s)					
Notice of References Cited (PTO-892)     Notice of Draftsperson's Patent Drawing Review (PTO-948)		ummary (PTO-413) )/Mail Date			
<ul> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)</li> <li>Paper No(s)/Mail Date 1/18/02</li> </ul>	_	formal Patent Application (PTO-152)			

## **DETAILED ACTION**

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3 Drawings

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The drawings are objected to under 37 CFR 1.83(a) because element 70 of the drawings fails to depict 'child key 1.2' as described in the specification. Any structural detail that is essential for a proper understanding of the disclosed invention should be shown in the drawing. MPEP § 608.02(d). Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

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1	Specification				
2	The disclosure is objected to because of the following informalities:				
3	Page 13, line 5 contains the misspelling of 'least key' as 'lease key'.				
4	Page 13, line 10 contains the variable 'V' in the probability equation. This				
5	variable is previously referred to as 'U' in the specification, line 7				
6	Appropriate correction is required.				
7					
8	Claim Rejections - 35 USC § 103				
9	The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all				
10	obviousness rejections set forth in this Office action:				
11 12 13 14 15 16	(a) A patent may not be obtained though the invention is not identically disclosed or described as se forth in section 102 of this title, if the differences between the subject matter sought to be patented at the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.				
17	Claims 1 – 3, 5 – 8, 10 – 13, and 15 are rejected under 35 U.S.C. 103(a) as				
18	being unpatentable over Trusted Computing Platform Alliance (TCPA), "Main				
19	Specification Version 1.0" in view of Challenger et al., "Algorithm for Cache				
20	Replacement", U.S. Patent 6,266,742 B1.				
21					
22	Regarding claim 6, TCPA discloses a trusted computing platform system,				
23	wherein exists a TPM ("computer module") designed to load, evict, and use				
24	cryptographic keys that are cached within the TPM (TCPA, pages 3, 5, 6; page 19,				
25	"TCPA_NOSPACE"; pages 38-40, 123-7, 136, 145-7). TCPA discloses that it is				
26	necessary for the TPM to utilize more keys than is allowable, due to constraints in				

cached in the TPM or evicted from the TPM.

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storage space. Therefore, the trusted computing platform system provides a means for managing the loading of keys into the TPM. The keys are stored, or cached, inside the TPM in the form of a tree hierarchy of parent and children keys. Necessary keys utilized by the TPM, but stored outside of the device, are placed in encrypted key blobs (TCPA, page 123, pars. 1-3; page 124, lines 1-10). TCPA does not disclose the method in particular used by the trusted computing platform to efficiently manage which keys

Challenger et al. discloses a method for managing objects stored in a cache.

Challenger et al. discloses a means for determining a replacement expense for each of a plurality of cached objects in memory. The replacement expense is used to determine the desirability of caching or evicting an object from memory (Challenger et al., Abstract; fig. 4). The replacement expense is determined by a probability that each said evictable object will be needed by the computer module after said evictable object is evicted (Challenger et al., Abstract, lines 7), and an amount of cycle time required to re-store, if evicted, each said evictable object in the computer module (Challenger et al., Abstract, lines 7, 8; col. 1, lines 22-31). Challenger et al. further discloses a means for identifying a least expensive evictable object based on said replacement expense, and means for replacing said least expensive evictable object with a replacement object (Challenger et al., fig. 4, elems. 420, 460; TCPA, page 123, par. 4).

It would have been obvious to one of ordinary skill in the art at the time of the invention to employ the method of Challenger et al. for efficiently managing a cache of stored objects with the system of TCPA for loading and evicting keys from a cache.

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1 This would have been obvious because one of ordinary skill in the art would have been

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2 motivated to manage the loading and evicting of objects ("keys") from the TPM in a

manner characterized by the efficient utilization of system processes.

Regarding claim 7, the combination of TCPA and Challenger et al. discloses:

means for locating a blob comprising said least expensive evictable cryptology

key and a security software shell; means for removing said security software shell from

said blob; and means for storing said least expensive evictable cryptology key in said

9 computer module (Challenger et al., page 124).

Regarding claim 8, the combination of TCPA and Challenger et al. discloses the cycle time is determined by calculating the time to fetch the object from a remote location (Challenger et al., col. 1, lines 28-32). Challenger et al. also discloses that it would be obvious to one of ordinary skill in the art to apply the calculation of cycle time to the caching of data stored in hierarchies (e.g. databases), thus – the calculation of time necessary for the organization of data objects into hierarchal data structures (Challenger et al., col. 3, lines 52-67). Therefore, the combination of TCPA and Challenger et al. discloses as obvious that fact that calculating the time necessary to fetch an object would include the time it takes to fetch the ancestors of which the object depends upon in the hierarchal data structure. Thus, the combination of TCPA and Challenger et al. disclose that the calculation of cycle time depends upon the generations of ancestors.

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2 Regarding claim 10, the combination of TCPA and Challenger et al. discloses:

3 wherein the computer module is a Trusted Platform Module (TPM) (TCPA, page.

4 123).

Regarding claims 1-3, 5, 11-13, and 15, they are the method and computer program product claims implemented by and corresponding to the system claims 6-8, and 10, and they are rejected for the same reasons.

Claims 4, 9, and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over the combination of Trusted Computing Platform Alliance (TCPA), "Main Specification Version 1.0" and Challenger et al., "Algorithm for Cache Replacement", U.S. Patent 6,266,742 B1 as applied to claims 1 – 3, 5 – 8, 10 – 13, and 15 above, and further in view of Deshpande et al., "Method of Reconstructing a Managed Information Tree", U.S. Patent 5,893,103.

Regarding claim 9, the combination of TCPA and Challenger et al. disclose a system for the loading (caching) of keys organized into hierarchal data structures into a TPM (see rejections of claims 6, 7, and 8). The combination of TCPA and Challenger et al. does not disclose in particular the method for the loading of a hierarchal structure of keys.

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Deshpande et al., discloses a method for replicating into memory a hierarchal structure of objects from a remote location. Deshpande et al. discloses a means for methodically loading and storing the ancestor objects of a child object until the hierarchal structure is established so that the child object itself may be loaded and stored (Deshpande et al., col. 4, lines 18-44).

It would have been obvious to one of ordinary skill in the art to employ the

method of Deshpande et al. for replicating in memory a hierarchal structure of data in the system of the combination of TCPA and Challenger et al. for the loading and caching of parent and children keys. This would have been obvious because one of ordinary skill in the art would have been motivated to employ a method enabling the loading of ancestor keys into memory so that a necessary child key may be loaded.

Regarding claims 4 and 14, they are the method and computer program product claims implemented by and corresponding to the system claims 9, and they are rejected for the same reason.

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1	Conclusion
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3	The prior art made of record and not relied upon is considered pertinent to
4	applicant's disclosure:
5	
6	P. Cao and S. Irani, "Cost-aware WWW proxy caching algorithms," in Proc.
7	USENIX Symp. on Internet Technologies and Systems, pp. 193206, December 1997.
8	Cherkasova et al., "Method for Cache Replacement of Web Documents", U.S.
9	Patent 6,546,473 B2.
10	Cherkasova et al., "Cashing Protocol Method and System Based on Request
11	Frequency and Relative Storage Duration", U.S. Patent 6,425,057 B1.
12	
13	A shortened statutory period for reply is set to expire 3 months (not less than 90
14	days) from the mailing date of this communication.
15	Any inquiry concerning this communication or earlier communications from the
16	examiner should be directed to Jeffery Williams whose telephone number is (571) 272-
17	7965. The examiner can normally be reached on 8:30-5:00.
18	If attempts to reach the examiner by telephone are unsuccessful, the examiner's
19	supervisor, Emmanuel Moise can be reached on (571) 272-3865. The fax phone
20	number for the organization where this application or proceeding is assigned is (703)
21	872-9306.

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1 Information regarding the status of an application may be obtained from the

- 2 Patent Application Information Retrieval (PAIR) system. Status information for
- 3 published applications may be obtained from either Private PAIR or Public PAIR.
- 4 Status information for unpublished applications is available through Private PAIR only.
- 5 For more information about the PAIR system, see http://pair-direct.uspto.gov. Should
- 6 you have questions on access to the Private PAIR system, contact the Electronic
- 7 Business Center (EBC) at 866-217-9197 (toll-free).

8

9

10 Jeffery Williams

- 11 Assistant Examiner
- 12 Art Unit 2137
- 13 06.23.2005

AYAZ SHEIKH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

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